

REMARKS

The present request for continued examination and amendment is in response to the Office Action dated May 11, 2006. Applicant cancels claim 10 and amends independent claims 1 and 2 and dependent claims 17-19 and 23-25.

A. Rejection under 35 U.S.C. §103(a)

The Examiner rejects the pending claims under 35 U.S.C. 102(a) as being unpatentable over Ito (US Patent No. 5,528,207) in view of one or more of Levine (US Publ. 2002/015779), Osadchy (US Patent No 6,621,377), as being unpatentable over Osadchy in view of Levine, or as being unpatentable over Brosn (US Publ. 2004/0095202) in view of Ito and Levine.

In response to the Examiners rejections on page 5 as to claim 10, on pages 6-7 as to claims 17-19, and on pages 8-9 as to claims 23-25, Applicant amends the independent claims 1 and 2 to include *structural limitations of the placement of the dielectric block/element with respect to the conductive trace/electrical component.* (See case cited by the Examiner, *Ex parte Masham*, 2 USPQ2d 1647(Bd. Pat. App. & Inter. 1987.) The cited references do not teach the structural limitations as claimed by Applicant in currently amended independent claims 1 and 2. Thus, Applicant asserts that the claims the cited references do not teach or suggest the claimed inventions of claims 1 and 2, and respectfully requests that the Examiner issue a notice of allowance for the pending claims.

In paragraphs [0019] and [0021], among others, of Applicant's specification, Applicant teaches that the dielectric material can be displaced or oriented to optimize an electrical property of the trace. Thus, Applicant structurally defines placement of the components in the claims as amended. Specifically, in claim 1, the dielectric block is mounted on the substrate with a block edge rotated to a target orientation with respect to the trace axis to obtain a desired electrical property. Similarly, in claim 2, the dielectric component is mountable on the PCB at a plurality of angles, and is mounted at a first angle to produce a desired modification of an electrical parameter.

The cited references do not teach or suggest the placement of the dielectric with respect to the conductive element to obtain the desired electrical property. Specifically,

Levine shows a fixed orientation with the dielectric 11 enveloping the internal conductors 13a (See e.g., Figure 4). Osadchy teaches a microstrip line (20) on the surface of an FE dielectric which connects to networks (34, 42) which sit on a second dielectric. (See, e.g., Figure 3) Osadchy does not teach modification of the electrical parameter based upon orientation of the dielectric with respect to the conductor. In fact, electrical parameter modification is achieved by biasing the FE material as shown in Osadchy. Levine teach a fixed design structure 100 (see figure 1) that also does not teach or suggest Applicant's claimed structure. Similarly, Brown's apparatus does not teach or suggest the structural elements of Applicant's invention. The cited references - alone or in combination - fail to teach or suggest a specific "orientation" of the dielectric with respect to the electrical element to obtain a desired electrical parameter in the electrical element as claimed by Applicant in independent claims 1 and 2. Thus, Applicant respectfully requests that the Examiner issue a notice of allowance for independent claims 1 and 2 and the claims dependent thereupon.

B. Conclusion

Applicant asserts that the pending claims are now patentable over the cited references, and Applicant respectfully requests that the Examiner issue a notice of allowance for all of the pending claims 1-9 and 16-25.

The fees for an RCE and a one (1) month extension of time is filed herewith by EFS. The Director is authorized to charge any additional fee(s) or any underpayment of fee(s) or credit any overpayment(s) to Deposit Account No. 50-3001 of Kyocera Wireless Corp.

Applicant requests that the Examiner telephone the attorney for Applicant at the telephone number listed below should the Examiner believe that prosecution of this application might be expedited by further discussion of the issues.

Respectfully Submitted,

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